

257	345
Class	Subclass
ISSUE CLASSIFICATION	

PATENT NUMBER

6492684

6492684

U.S. UTILITY Patent Application

<p><i>MR</i></p> <p>O.I.P.E.</p> <p>SCANNED <u>3800</u> O.A. <u>41</u></p>	<p>PATENT DATE</p> <p>REDACTED</p> <p>REDACTED</p> <p>DEC 2002</p>
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APPLICATION NO. 09/878681	CONT/PRIOR D	CLASS 257	SUBCLASS 240	ART UNIT 2811	EXAMINER R23/11/2009
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APPLICANTS

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Subhash Kulkarni
Dominic Schepis

TITLE

Silicon-on-insulator chip having an isolation barrier for reliability

PTO-2040
12/99

ISSUING CLASSIFICATION									
ORIGINAL		CROSS REFERENCE(S)							
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)						
257	3K9	257	3K7	3K0	352	353	355		
INTERNATIONAL CLASSIFICATION		257	374	375	376				
H01L	29/01								

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg. <i>15/6</i>	Figs. Drwg. <i>18</i>	Print.Fig. <i>2C</i>	Total Claims <i>38</i>
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED <i>7/14/02</i>	
	<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____		ISSUE FEE <i>1w</i> Amount Due <i>#1,580</i> Date Paid <i>10-9-02</i>	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	<i>Abraham</i> (Primary Examiner) <i>7/11/02</i> (Date)		ISSUE BATCH NUMBER	
	<i>Shurt</i> (Legal Instruments Examiner) <i>7/18/02</i> (Date)			
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